IN THE CLAIMS:

1. (Previously Amended) An array substrate comprising:

a display area in which pixel electrodes are formed,

a scanning line formed of a low resistivity metal, said scanning line being arranged between the pixel electrodes,

a signal line formed of a high melting point metal selected from the group consisting of chrome, molybdenum, tantalum and alloys thereof, said signal line crossing over the scanning line interposing an insulating layer therebetween,

a terminal to which a scanning signal is applied, and

an extended scanning line for connecting the scanning line with the terminal, said extended scanning line being formed only of the same conductive film as for said signal line.

2. (Previously Amended) The array substrate of claim 1 comprising:

an auxiliary capacitance line arranged parallel to the scanning line,

a collected auxiliary capacitance line arranged in parallel to the signal line and electrically connected to the auxiliary capacitance line,

a terminal to which a common signal is applied, and

an extended auxiliary capacitance line for connecting the collected auxiliary capacitance line with the terminal for the common signal, said extended auxiliary capacitance line being formed only of the same conductive film as for said signal line.

3. (Previously Amended) An array substrate comprising:

a display area in which pixel electrodes are formed,

a scanning line formed of a low resistivity metal, said scanning line being arranged between the pixel electrodes,

an auxiliary capacitance line arranged in parallel to the scanning line,

a signal line formed of a high melting point metal selected from the group consisting of chrome, molybdenum, tantalum and alloys thereof, said signal line crossing over the scanning line and the auxiliary capacitance line interposing an insulating layer therebetween,

a collected auxiliary capacitance line arranged in parallel to the signal line and electrically connected to the auxiliary capacitance line,

a terminal to which a common signal is applied, and

an extended auxiliary capacitance line for connecting the collected auxiliary capacitance line with the terminal, said extended auxiliary capacitance line being formed only of the same conductive film as for said signal line.

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5. (Previously amended) The array substrate of claim 1, wherein the extended scanning line is formed only of the same conductive film as for the pixel electrodes, instead of the same conductive film as for said signal line.

6. (Currently amended) The array substrate of claim 1 4, wherein the extended scanning line is electrically connected to the scanning line at the neighborhood of the display area and electrically connected to the terminal for the scanning signal at the neighborhood of the terminal.

8. (Previously Amended) The array substrate of claim 2, wherein the extended auxiliary capacitance line is formed only of the same conductive film as for the pixel electrodes, instead of the same conductive film as for the signal line.

9. (Previously Amended) The array substrate of claim 8, wherein the extended auxiliary capacitance line is electrically connected to the collected auxiliary capacitance line at the neighborhood of the display area and electrically connected to the terminal for the common signal at the neighborhood of the terminal.

10. (Previously Amended) The array substrate of claim 2, wherein the auxiliary capacitance line, the collected auxiliary capacitance line and the scanning line are formed from the conductive film of same layer.

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11. (Currently Amended) The array substrate of claim 2, wherein the collected auxiliary capacitance line and the extended scanning line are crossing, interposing an insulating layer therebetween.

12. (Original) The array substrate of claim 1, wherein aluminum or aluminum alloy is used for material of the scanning line.

13. (Original) The array substrate of claim 1, wherein partly or wholly nitridated aluminum or partly or wholly nitridated aluminum alloy is used for material of the scanning line.

14. (Cancelled NOV. 2002)

15. (Original) The array substrate of claim 1, wherein the scanning line and the extended scanning line are electrically connected via a conductive film of the same layer as that for the pixel electrode.

16. (Original) The array substrate of claim 2, wherein the auxiliary capacitance line and the extended auxiliary capacitance line are electrically connected via a conductive film of the same layer as that for the pixel electrode.

17. (Original) The array substrate of claim 1, wherein either of the scanning line or the extended scanning line is formed in a grid or ladder like shape at a region in which the scanning line and the extended scanning line are overlapped within a connecting portion between the scanning line and the extended scanning line.

18. (Original) The array substrate of claim 2, wherein either of the collected auxiliary capacitance line or the extended auxiliary capacitance line is formed in a grid or ladder like shape at a region in which the collected auxiliary capacitance line and the extended auxiliary capacitance line are overlapped within a connecting portion between the collected auxiliary capacitance line and the extended auxiliary capacitance line.

Claims 19-21 were cancelled on 26 NOV. 2002.